Project 2 Part B

CPI = (2 \* R\_IC + I\_IC + 3 \* J\_IC) / Total\_IC

Energy\_Consumed = ALU\_Count + 0.5 \* Jump\_Count + 3 \* Branch\_Count + 10 \* Memory\_Count + 1.5 \* Other\_Count (fJ)



Sentences:

* this is a sentence here
* The number of capital letters present in these words being
* You will be expected to apply selected MIPS assembly language instructions, assembler directives and system
* This sentence needs twenty words for the chart to be completed and now there are no more lab for today
* The MIPS P5600 CPU is based on the MIPS32 ISA architecture, supporting up to six cores achieving the industry's leading CoreMark/MHz score per core. It supports high performance data parallel operations such as DSP, imaging and media.